TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

# 2SK2884

#### Chopper Regulator, DC-DC Converter Applications

• Low drain–source ON resistance :  $R_{DS (ON)} = 1.9 \Omega (typ.)$ • High forward transfer admittance :  $|Y_{fs}| = 3.8 S (typ.)$ 

Low leakage current :  $I_{DSS} = 100 \mu A (max) (V_{DS} = 640 V)$ 

• Enhancement mode :  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	800	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	800	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	5	Α	
	Pulse (Note 1)	I <sub>DP</sub>	15	Α	
Drain power dissipation (Tc=25°C)		$P_{D}$	100	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	370	mJ	
Avalanche current		I <sub>AR</sub>	5	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	10	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	−55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.25	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

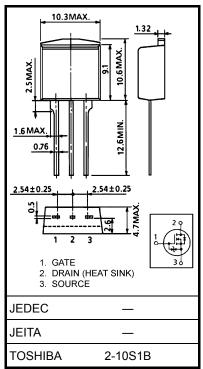
Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 27 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

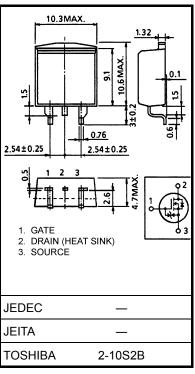
This transistor is an electrostatic-sensitive device.

Please handle with caution.

Unit: mm



Weight: 1.5 g (typ.)



Weight: 1.5 g (typ.)



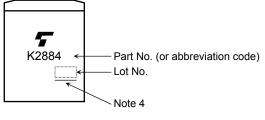
## **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	_	±10	μA
Gate-source bro	eakdown voltage	V <sub>(BR)</sub> GSS	$I_G = \pm 10 \mu\text{A},  V_{DS} = 0 \text{V}$	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 640 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	800	_	_	V
Gate threshold v	/oltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A	_	1.9	2.2	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 3 A	1.0	3.8	_	S
Input capacitano	ce	C <sub>iss</sub>		_	1080	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		16	_	pF
Output capacitance		Coss		_	105	_	
Switching time	Rise time	t <sub>r</sub>	$V_{\rm GS}$ $V_{\rm OUT}$ $V_{\rm DD}$ $V_{\rm OUT}$ $V_{\rm DD}$	_	40	_	
	Turn-on time	t <sub>on</sub>		_	80	_	ne
	Fall time	t <sub>f</sub>		_	40	_	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{W}} = 10 \mu s$	_	140	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	34	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		16		nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>		_	18	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	ldr	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	15	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V	1	1000	1	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> / dt = 100 A / μs		7.5		μC

## Marking

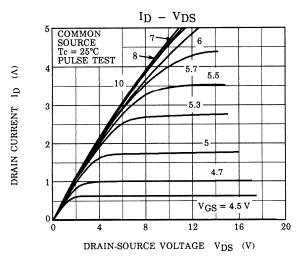


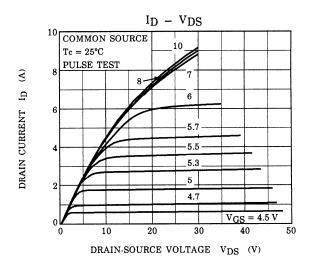
Note 4: A line under a Lot No. identifies the indication of product Labels.

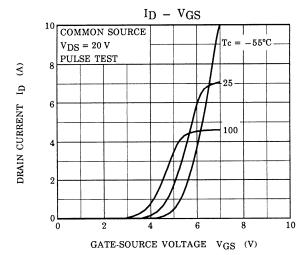
Not underlined: [[Pb]]/INCLUDES > MCV

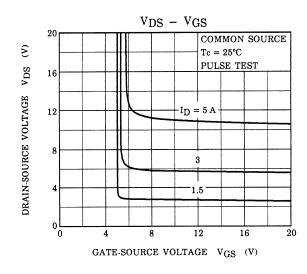
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

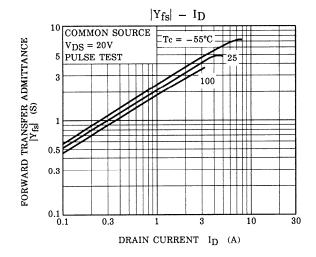
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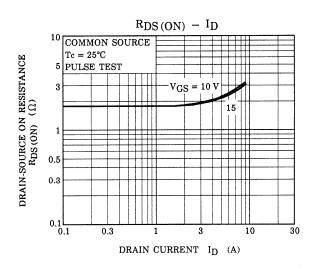




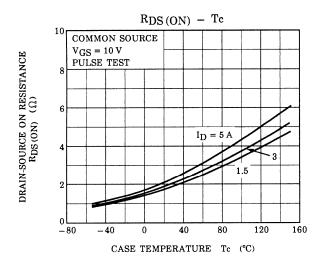


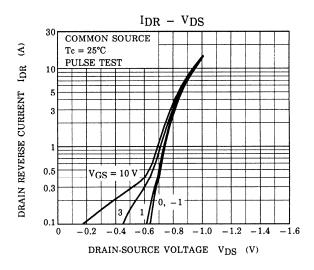


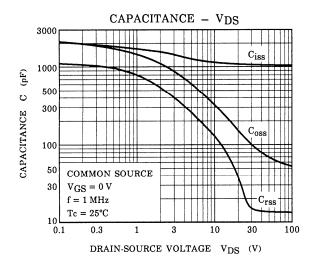


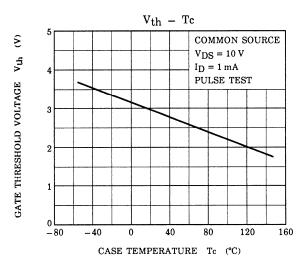


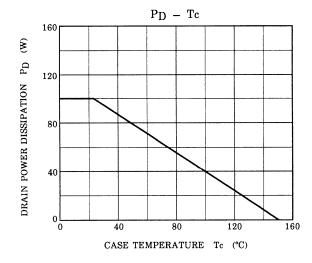
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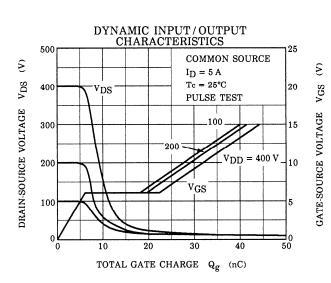


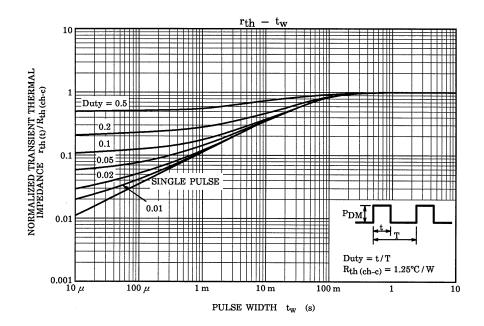


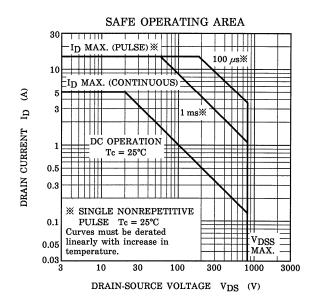


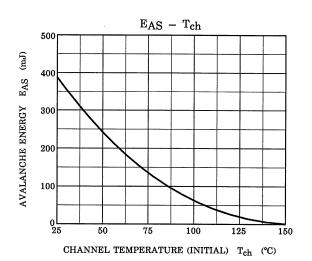


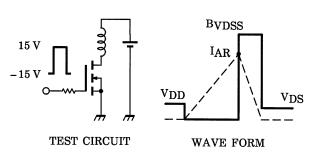












$$R_G = 25 \Omega$$
  
 $V_{DD} = 90 V$ ,  $L = 27 mH$ 

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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